



IWWE6

mmWave Permittivity Standard Reference Material Development













Review of the iNEMI project objectives and goals in a view of 5G industry needs for traceable materials' characterization

Say Phommakesone (Keysight Technologies, USA)

Review of the efforts on developing traceable permittivity standard reference material

Nate Orloff (NIST, USA)

Review of the iNEMI round-robin SRM candidate material characterisation results and challenges

Marzena Olszewska-Placha (QWED, Poland)

5G/6G mmWave Materials and Electrical Test Technology Roadmap

Urmi Ray (INEMI, USA)





Acknowledgement



Special thanks to the entire working group

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Review of the iNEMI project objectives and goals in a view of 5G industry needs for traceable materials' characterization

Say Phommakesone (Keysight Technologies)

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5G/mmWave Materials Assessment & Characterization

Motivation:

- 5G Solutions require ultra-low loss laminate materials and PCBs/substrates for efficient design of 5G communications equipment
- The existing transmission loss or Df/Dk measurement methodology lacks consistency, especially for higher frequency measurements (such as 30-100GHz)
- Many different test methods currently in the industry, require different fixtures and test methods/sample preparation/data analysis/extraction
- Industry needs standardized measurement methods and reference materials

Objective:

- Develop consistent Df/Dk measurement methodologies for characterizing ultra low loss laminate materials in the range of 30 – 100GHz
- Provide guidelines and best practices to the industry

Strategy/Approach:

- Benchmark existing measurement methodologies and reference materials
- Develop guideline of standardized method of Dk, Df measurement based on round robin testing
- Propose "standard" test coupons for industry wide application (Phase 2)





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Industry Collaboration Brought Together by iNEMI

Approach for Solving Problem:

- Bring together Cross-functional team spanning industry Value Chain
- iNEMI Project team members cover wide range of industry, academia and equipment suppliers **Deliverable:**
- Develop a guideline/best practice for a standardized measurement and test methodology that can be shared with industry and relevant standards organizations
 - 3M
 - AGC-Nelco
 - Ajinomoto USA
 - AT&S
 - Centro Ricerche FIAT-FCA
 - Dell
 - Dupont
 - EMD Electronics (Co-Chair)
 - Flex

Project Team

- Georgia Tech
- Showa Denko Materials
- IBIDEN Co Ltd
- IBM
- Intel
- Isola
- ITRI (Co-Chair)
- Keysight (Co-Chair)
- MacDermid-Alpha

- Mosaic Microsystems
- NIST
- Nokia
- Panasonic
- QWED
- Shengyi Technology Company
- Sheldahl
- Unimicron Technology Corp
- Zestron





iNEMI 5G Project Team – Spanning across Supply Chain



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ITRI

CFT



EEE MICROWAVE THEORY &

iNEMI 5G Materials Project Members







Project Goals



- Gather industry experts to understand needs and address these problems
- Development of traceable material references by standards organizations
- Linkage between end-to-end supply chain: materials suppliers, equipment manufacturers and end users

Task 1	Task 2	Task 3	Task 4
 Benchmark Current techniques Typical material samples Potential reference materials Common practices & issues 	BenchmarkEmerging techniquesPossibilities beyond 100GHz	 Round Robin Tests Create reference samples Test metrology differences Study lab to lab variations 	Extension to advanced substrate materials - Commercial material testing
Report complete 🗸	Report complete 🛛 🗸	Report complete 🗸	Report complete 🛛 🗸

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Reports can be downloaded from project site, using iNEMI login https://www.inemi.org/5g-mat-assessment





Task 1 Report Oct 2021

Benchmarking Available Measurement Methods









Task 1: Benchmark Current Measurement Techniques

- Each new material for mmWave 5G applications requires careful consideration to determine the best measurement methodology, fixture, sample fabrication and test instrument.
- There are dozens of different methodologies that could be used, but which to choose is often not obvious.
- iNEMI Report focuses on the following *resonator based* measurement techniques: split-post dielectric resonator (SPDR), split cylinder resonator (SCR), balanced circular disk resonator (BCDR), and Fabry-Perot open resonator (FPOR).

Map of available techniques vs Frequency







Resonator-based measurement

Split-post dielectric resonator (SPDR) Split cylinder resonator (SCR)

Balanced-type circular disk resonator (BCDR)

Discrete frequency points from 1 GHz up to 15 GHz Discrete frequency points from 10 GHz up to 80 GHz Multiple discrete frequencies from 10 GHz up to 120 GHz Fabry-Perot open resonator (FPOR, also called opencavity)

Discrete frequencies between 20 GHz up to 110 GHz















Resonator-based measurement

SPDR

- High measurement precision
- Easy to use
- Insensitive to many user errors
- In-plane component of permittivity (typically)
- Typically extrapolated to 5G mmWaves
- Typical sample thicknesses less than 1 mm
- IEC 61189-2-721:2015
- <u>https://www.qwed.com.pl/resonators_spdr.html</u>
- <u>https://www.keysight.com/us/en/assets/7018-</u> 01416/application-notes/5989-5384.pdf

SCR

- High measurement precision
- Can be sensitive to user errors
- Typically interpolated to 5G mmWaves
- In-plane component of permittivity (typically)
- Typical sample thicknesses around 100 um
- Support temperature sweep measurement
- IPC-TM-650 2.5.5.13
- <u>https://www.keysight.com/us/en/assets/7018-</u> 06384/brochures/5992-3438.pdf







Resonator-based measurement

BCDR

- High measurement precision
- Requires full 2-port calibration (mechanical to 110 GHz or electrical to 67 GHz)
- Out-of-plane component of permittivity (typically)
- Typical sample thicknesses less than 1 mm
- IEC 63185
- <u>https://www.keysight.com/us/en/assets/7120-</u> <u>1214/flyers/N1501AE11-67-Balanced-Type-Circular-Disk-</u> <u>Resonator-BCDR.pdf</u>

FPOR

- High measurement precision
- Can be sensitive to user errors
- Uncertainty increases with increasing frequency
- In-plane component of permittivity (typically)
- JIS R1660-2
- <u>https://www.qwed.com.pl/resonators.html#ResonatorFPOR</u>
- <u>https://www.keysight.com/main/editorial.jspx?cc=US&lc=en</u> <u>g&ckey=2276755&nid=null&id=2276755</u>







Task 2 Report Nov 2021

Benchmarking EMERGING Measurement Methods









Wafer-Level Measurements and Time Domain Techniques

Microstrip ring resonator (MRR) and coplanar waveguide (CPW) methods



Time domain spectroscopy





Task 2 Key Takeaway



- Multiple different resonator-based measurement techniques exist
- Techniques are based on complex physics and require careful sample preparation and attention to "standard operating procedures", including, but not limited to:
 - Standard reference material
 - Sample Thickness measurement
 - Effect of room temperature and humidity
- Recommendation:
 - Interlaboratory comparison of resonator-based measurement techniques (Round Robin study) – Task 3







Task 3

Global Interlaboratory Comparison Round Robin Study 1 and 2







Round Robin Overview – Task 3



Sample Material Requirements

- Stable, Low loss
- Low moisture absorption / temperature dependency
- Isotropic
- Good mechanical & handling properties

Selection for RR1 and 2

- Precision Teflon
- Cyclo Olefin Polymer

Future additions

- Rexolite
- Fused Silica



9/10 Laboratories Effort





Reference Sample



Survey of commercial resonators and frequency space resulted in choice of two sample sizes to cover frequency range 10GHz to 100GHz)

Sample	Sample Size	Reference
Teflon 50um	35 mm x 45 mm	DuPont™ Teflon® FEP, Type A
Teflon 125um	35 mm x 45 mm	DuPont™ Teflon® FEP, Type A
Teflon 50um	90 mm x 90 mm	DuPont™ Teflon® FEP, Type A
Teflon 125um	90 mm x 90 mm	DuPont™ Teflon® FEP, Type A
COP 188um	90 mm x 90 mm	Zeonex Cycloolefin Polymer ZF14- 188
COP 188um	35 mm x 45 mm	Zeonex Cycloolefin Polymer ZF14- 188





Round Robin Overview – Task 3



10 Kits Created

- Sample sizes 35 mm x 45 mm, 90 mm x 90 mm
- 9 International labs participating

Techniques included

- Split Post Dielectric Resonator
- Split Cavity Resonator
- Fabry-Perot
- Balanced Circular Disk Resonator

Frequency Span : 10GHz – 100GHz

Goals

- 1. High level how much agreement is there in results
- 2. Thickness how closely do labs assess thickness
- 3. Understand sample quality, handling and sizing / compatibility
- 4. Understand practicality of techniques
- 5. Look for obvious biases between equipment types
- 6. Look for frequency dependency at higher frequencies
- 7. Do we need traceable standards?







Task 3: Round Robin Experiment





Round Robin Test Labs

3M: USA Dupont:USA Intel: USA ITEQ: Taiwan ITRI: Taiwan QWED: Poland

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Keysight: USA Nokia: Finland NIST: USA Showa-Denko: Japan Shengyi Electric: China



COP Results







Teflon Results





Almost 'ideal' samples:

- \pm 2% Range for ϵ_r
- ±0.0002 to ±0.0004 for TanD
- Unknown accuracy



Is this good?



- Better than many expected
 - Lots of potential lab variations yet results are very good for 'perfect' samples
 - Even within a lab $\pm 2\%$ can be challenging for complicated metrologies
- But for many applications this is not good enough
 - $\pm 2\%$ error could cause design cycle customer & supplier could differ by 4%
 - Offset mean values can impact HVM distribution tails & DPMs
 - Intel example this would cause noticeable miscorrelation with design
 - Real, imperfect samples worse
- Reproducibility does not ensure accuracy
 - Certified standard references still needed
 - Accuracy is very important; we have no way to assess it today



A Year of effort to do this study. If SRM was available, labs would have been able to validate measurement tools independently





Goals – how did we do?



- High level how much agreement is there in results? Best case samples – don't expect better than ±2%, real samples worse
- **2.** Thickness how closely do labs assess thickness? Labs using similar micrometers and averaging yields ~0.2-0.5% differences in numerical results
- **3.** Understand sample quality, handling and sizing / compatibility Sample quality, TTV & flatness is a key limiter, equipment compatibility is challenging
- **4.** Understand practicality of techniques Limited experience / skills & speed – SCR & SPDR, benefits of FPOR, BCDR require more experience
- 5. Look for obvious biases between equipment types No obvious biases with given sample sizes, except in industrial materials where anisotropy is visible
- 6. Look for frequency dependency at higher frequencies COP & Teflon appear to be very constant at mmWave frequencies, however, without traceability the confidence in this statement is not clear and we have to rely heavily on BCDR and FPOR results for this claim

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7. Do we need traceable standards?

Yes – Further progress in this space is difficult without a high confidence, known reference





Round 2 Kit Alignment



- Kit alignment is complicated
 - Sample size compatibility vs method vs lab tools
 - Want good mix of sample method lab to get good overlap

Round 1:RoundKit 2 NIST 6 samples measured, 1 methods, max 40GHzKit 2-Kit 3 Keysight 12 samples measured, 2 methods, max 80 GHzKit 3-Kit 4 QWED 12 samples measured, 2 methods (*FP), max 110Kit 4-Kit 5 ITRI 6 samples measured, 2 methods, max 60GHzKit 5-Kit 6 ITEQ 12 samples measured, 2 methods, max 40GHzKit 6-Kit 7 Nokia 2 samples measured, 1 methods (*BCDR), max 46GHz (90x90)Kit 7-Kit 8 Shengyi Electric 12 samples measured, 1 methods, max 15GHzKit 8-Kit 9 Showa Denko 6 samples measured, 2 methods (*BCDR), max 110GHz (45x35)Kit 9-

Round 2: Kit 2->Keysight Kit 3->NIST Kit 4->Showa Denko Kit 5->Nokia Kit 6->Shengyi Electric Kit 7->ITRI Kit 8->ITEQ Kit 9->QWED

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Selection seems to give a reasonable compromise between all the constraints





RR 2 Status



- Qualitative ideas we ultimately want to check:
 - Average thickness assessment matters but may not be totally dominant in variation
 - Lab to lab differences for a specific sample seem less than assuming all samples are the same
 - Do we see frequency dependence in these materials above 10GHz
- "Blind" assessment of material
 - $\pm 2\%$ for ϵ_r
 - ±0.0002 to ±0.0004 for TanD (for these very low loss samples may not translate to typical materials)





2.40

2.38

2.36

All COP Measurements

Current Full Data Set (Both Rounds)

Intel SPDR(i)

Intel SCR(i) Keysight SCR(L)

×

•

2.08 -

2.06

2.04













Task 4: "Commercial material testing"

Materials are real, practical substrate materials submitted by industry representatives in the iNEMI group. Also included are Reference samples: Fused Silica and Rexolite

Task 4 Goal:

Seeks to validate the findings of Task 3 by showing that these same measurements are effective on real materials from multiple sectors of industry.





The electronics sample kit looks like

Fused

Silica

samples



Electronics sample kit: 34 samples from 7 sources



- Thicknesses: 60 um 300 um
- Dielectric constants: 2.2 5
- Isotropic and anisotropic
- samples

35 mm x 45 mm samples

The other kit included some more challenging samples...



The automotive sample kit



- Thicknesses: up to 4 mm
- Composites (thin film on substrate)
- Limited measurement options



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Automotive sample kit: 17 samples





Limited Number of Test Labs for Efficiency and Schedule



Samples submitted by global project members from USA, EU and Asia

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- Keysight
- Intel
- NIST
- QWED
- Nokia

EU Labs

US Labs



Task 4 Key takeaways



- Two sample sizes sufficient to cover all considered test methods (35 mm x 45 mm and 90 mm x 90 mm)
- Accurate thickness evaluation and low thickness variation are of high importance
- Results variation of 2% 5%

• Standard reference materials are of high interest





Disclaimers to the Task 4 results



- 1. Limited sample size
 - 5 labs total, 3 labs max on one physical sample
 - Compared samples cut from the same material
 - Significant overlap in instrumentation
- 2. We are all measuring the same kit
 - Sample-to-sample variation is not as well captured
- 3. We are all using the same thickness measurements*
 - Most direct possible comparison of microwave measurements
 - Removes variation coming from different thickness measurement capabilities between labs







The electronics samples expanded range of Er' and TanD values

ward New Concerning Co	[may an unit a state a
Real part of permittivity	Loss tangent
I ask 3	
range	

35

In just the electronics samples, we cover broad range of frequencies and materials




Summary – Task 4



- We are able to measure real materials as well as PTFE & COP
- Results variation across labs is on average +/- 2% for real materials
- Thickness measurement challenges
 - \bigstar 1 um error on 100 um sample leads to 1% change in measured ϵ
- No unexpected challenges (handling/set up, shelf life) with thickness (60 um 300 um) or permittivity (2 4)
- Only 2 sample geometries needed
- More challenging materials can be measured with specific measurement setups





Limitation – Lack of Standards



- No NIST traceable standards exist
- Previously available standard discontinued

Why not redevelop previous SRM?

SRM 2870 Permittivity & Loss Tangent

Material Details

SRM 2870 - Relative Permittivity and Loss Tangent 1422 Cross-Linked Polystyrene

C - Certificate M - MSDS T - Table

Note: This material is not available for purchase.

- C <u>Certificate</u>
- Material Safety Data Sheet (MSDS)

Related Materials: 208.2 - Electrical Properties of Dielectrics

Currently Available for Purchase!



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Previous Generation NIST SRM SRM 2870



- Too thick for low loss mmWave methods shown below
- Required difficult to source machining to thin sample, ruining traceability
- X-Y dimensions of thinned sample still incompatible with most tools
- Certified only at 10 GHz too low for mmWave / 5&6 G



SRM 2870 Permittivity & Loss Tangent

Today, all permittivity tool vendors are operating without traceable standards for validating tool sets







Where do we go from here?

- New iNEMI project "5G Reference Material Development"
- Project kick-off Jun 16, 2022
- Contact Urmi Ray for further info) <u>urmi.ray@inemi.org</u>











Thank you for the attention









Review of the efforts on developing traceable permittivity standard reference material

Nate Orloff (NIST)



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National Institute of Standards and Technology U.S. Department of Commerce

Complex permittivity standard reference materials

Nate Orloff

Lucas Enright, Nick Jungwirth, Bryan Bosworth, Chris Long, Jim Booth

NIST Semiconductor Series

Semiconductor manufacturer's measurement problem





iNEMI 5G Materials Characterization Project Report I: "The lack of traceable reference material for mmWaves is a very serious problem. This lack makes verification of measurement methods and laboratory techniques impossible in an industry setting."

SRC Research Needs: Packaging

"Dielectric characterization up to 500 GHz and beyond. Scope includes anisotropic and inhomogeneous materials... High-frequency and high-temperature dielectric characterization of low-loss materials (encapsulants, mold compounds, substrates, etc.)."

Dielectric properties impact all microelectronics manufacturing

Semiconductor manufacturer's measurement problem

Semiconductor Industry Association



iNEMI 5G Materials Characterization Project Report I: "The lack of traceable reference material for mmWaves is a very serious problem. This lack makes verification of measurement methods and laboratory techniques impossible in an industry setting."

SRC Research Needs: Packaging

"Dielectric characterization up to 500 GHz and beyond. Scope includes anisotropic and inhomogeneous materials... High-frequency and high-temperature dielectric characterization of low-loss materials (encapsulants, mold compounds, substrates, etc.)."

Intel even gave 3 talks to NIST about this problem last

The problem is there's no standard for

dielectric constant

dated SRM Shipping Inf	ormation 4/3/2020 ***	Login	My Account View Cart Checkout
earch for Materials	Search Results		Select Language
M/RM Number:	SRM/RM Number:	GO	Policica by overgree manistate
earch	Keywords: dielectric	GO	Customer
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rowse the ORS ORS Home SRM Home Price List Archived Certificates	No longer available from NIST Note: These SRMs are no longer sold by NIST. They have either been material or discontinued from sale. You can find certificates and other deeither on their details page or in the Archived Certificates section of the SRM Description SRM Description State of the St	replaced (superseded) by a new b ocumentation relevant to these pro web site. tatus iscontinued	SRM's Not Allowed for We Orders For SRM's that cannot be ordered online, please ema SRM Sales Office at srminfo@nist.gov

The problem is there's no standard for

dielectric constant

Material Measurement I Standard Refe SRM Online Request S	Laboratory rence Materials System National Ins	stitute of Standards and Technology
Search for Materials SRM/RM Number:	SRM/RM Number: GO	Select Language
Search Keywords:	Keywords: dielectric GO	Customer Not Logged In
Search Browse the ORS	No longer available from NIST Note: These SRMs are no longer sold by NIST. They have either been replaced (superseded) by a new b	SRM's Not Allowed for Web Orders
	material or discontinued from sale. You can find certificates and other documentation relevant to these pro- either on their details page or in the <u>Archived Certificates</u> section of the web site.	oducts For SRM's that cannot be ordered online, please email SRM Sales Office at srminfo@nist.gov
	774 Lead-Silica Glass for Dielectric Constant and ac Loss Discontinued	Norton
	2870 <u>Relative Permittivity and Loss Tangent 1422 Cross-Linked</u> Discontinued <u>Polystyrene</u>	powered by Symantec ABOUT SSL CERTIFICATES

"These SRMs and no longer sold by NIST ..."

Why are semiconductor standards so important?



Without a standard industry's measurements are broken

Why are semiconductor standards so important?



Without a standard industry's measurements are broken

What can happen without a standard ...



$10.0~\%$ Error in ϵ_r		
Extra Cost/CPU	\$0.34	
CPU Volume	100 million units	
Additional cost	\$34 million	

Credit: M. Hill, Intel

What can happen without a standard ...



$10.0~\%$ Error in ϵ_r		
Extra Cost/CPU	\$0.34	
CPU Volume	100 million units	
Additional cost	\$34 million	

Credit: M. Hill, Intel

10% is not crazy



Credit: iNEMI round robin

This traceability gap is an opportunity for NIST



Don't worry we are going to break down all these steps

How can we fill the traceability gap?



Don't worry we are going to break down all these steps

iNEMI's special report identified fused silica



silica

iNEMI's special report identified fused silica



silica



We chose SCR. It has the shortest traceabili path.



The dimensions of the cavity and sample are the critical features

Theory* relates what we measure to what we want *this is not exactly the theory we want



Red = From dimensional metrology and Blue = From fitting Sparameters

Theory* relates what we measure to what we want *this is not exactly the theory we want



Red = From dimensional metrology and Blue = From fitting Sparameters

Theory* relates what we measure to what we want *this is not exactly the theory we want



Red = From dimensional metrology and Blue = From fitting Sparameters

We use a network analyzer to measure the data



Here is what the cavity looks like with <u>no</u> sample



The sample moves the peak to the left and down



The sample moves the peak to the left and down



The sample moves the peak to the left and down



We think* a white chromatic light sensor is best



(5)





We can't assume the index and must measure from both sides

We think* the best way is red-light interferometer



We can't assume the index and must measure from both sides

We think* that a CMM is best for the cavities



Measure dimensions



We need the dimensions of the cavity to about 1 µm at a few points

We think* that a CMM is best for the cavities

5 Measure

dimensions



We need the dimensions of the cavity to about 1 µm at a few points

We think* that a CMM is best for the cavities



We need the dimensions of the cavity to about 1 µm at a few points

Here are the dimensions for our $28~\mathrm{GHz}$ cavity



K	0.71100 ± 0.001 mm
S	6.89650 ± 0.001 mm
L	4.69874 ± 0.001 mm
R	7.60784 ± 0.0002 mm

We are getting about the 27.771 GHz for the empty cavity
This path is not as far off as you might imagine



We made 7 prototypes already ...

What specifications do we have to hit?



What specifications do we have to hit?



What specifications do we have to hit?



Here is the first prototype SRM 42

#2 of 7 actual prototypes



Lucas Enright





What other traceability chains can use this?



S-parameters, RF power, Antennas, Liquids, and more?

Only NIST can fill the dielectric

traceability gap

pdated SRM Shipping In	formation 4/3/2020 ***	Log	in My Account View Cart Checkout
Search for Materials	Search Res	sults	Select Language
RM/RM Number:		SRM/RM Number: GO	
Search		Keywords: dielectric GO	Customer
eywords:	T - Table		Not Logged In Items in Cart: 0
Browse the ORS	Currently available from NIST		SRM's Not Allowed for V Orders For SRM's that cannot be
ORS Home SRM Home Price List	SRM 42	Ultra high purity fused silica for dielectric constant and tangent from 1 GHz to 1 THz	IOSS ordered online, please em SRM Sales Office at srminfo@nist.gov
Archived Certificates			Powered by Symantec

Summarizing this talk ...

Objective: Develop a standard for dielectric constant and loss

State-of- New split cylinder cavities for dielectric

Key idea: New high purity fused silica with ultra low TTV

Impact: Enables metrology capability analysis and acceptance

Risks: Dimensional uncertainties are too high; samples are

Metrics: Dielectric constant and loss tangent to better than

Thanks

And thanks to iNEMI Urmi, Mike (s), Say, Malgorzata, Marzena, Chiawen, Lucas, Bryan, and the whole team!



Do dielectric materials matter to you?



Lucas Enright



You can participate in the new iNEMI project making a $5 \, {
m G}$ SRM

Mostly on wafer

Fabry Perot Anisotropic in of plane sapphire

BCDR Out of plane Fused silica

SCR In plane

What is the research staircase?



S-parameters, RF power, Antennas, Liquids, and more?

Here is the first prototype SRM $4242\,$

This is a 4" FS wafer



Lucas Enright



On-water lets us extend these ideas above 100 GHz



A semiconductor SRM can fill this traceability gap



Intel's problem scales with the # of dielectrics



Credit: M. Hill, Intel

Propagating the uncertainty through the theory

TABLE I ERROR CONTRIBUTIONS FOR THE QUARTZ SAMPLE



*This is specific to this cavity and not general

And that's it we have traceability ...

Use theory to map to permittivity

3

Measuring a sample is easy (Side view)



no sample

sample

We plan on using the SCR to start because it is the simplest

There are many different types of cavity resonators



We chose SCR because it has the shortest traceability

path

We can't bring SRM 2870 back...



1. It is too thick 2. It is too small 3.We cannot fabricate on it



The y-axis can also be wrong





Review of the iNEMI round-robin SRM candidate material characterisation results and challenges

Marzena Olszewska-Placha (QWED)



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Benchmarking project



- Benchmarking existing dielectric materials characterization methods
- Associating standardization institutes, electronic industry, equipment vendors, etc.
- Investigating repeatability and reproducibility
- Known and industrial materials

• Characterization techniques: SPDR, SCR, BCDR, FPOR





Benchmarking project (2)



- Four resonant methods:
 SPDR, SCR, BCDR, FPOR
- 3 material types
- 12 samples (6 in each of two sizes)

- 10 samples kits
- 11 labs
- Following common thickness acquisition procedure









SCR





Benchmarking results (1)



Repeatability studies

Each measurement repeated 16 times

Repeatability bounds: $\pm 3\sigma$

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M. Celuch, M.J. Hill, T. Karpisz, M. Olszewska-Placha, S. Phommakesone, U. Ray, B. Salski, "Benchmarking of GHz resonator techniques for the characterisation of 5G/mmWave materials", 51st European Microwave Conference April 2021, pp. 568-571.





EEE MICROWAVE THEORY &

Benchmarking results (2)



INEMI 5G/mmWave Materials Assessment and Characterization project results

Over 1000 measurement points in total



Round 1

Spread of Dk: $\pm 2\%$

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Benchmarking results (3)



INEMI 5G/mmWave Materials Assessment and Characterization project results



Spread of Dk: ±4%

99



Benchmarking results (4)





Connecting Minds. Exchanging Ideas. Characterisation of "real" materials



- Industry samples provided by the members of Project Consortium
- 2 types of material samples: electronic and automotive

Over 50 samples in total

🛛 5 labs

4 measurement techniques (SPDR, SCR, FPOR, and BCDR)



Connecting Minds. Exchanging Ideas. Characterisation of "real" materials (2)



Electronic materials







Observations and conclusions



- Two sample size sufficient to cover all considered test methods
- Accurate thickness evaluation is of high importance
- Thickness variation and sample flatness determine uncertainty of Dk extraction
- Results variation across the labs of $\pm 2\%$

• Standard reference materials are of high interest





Standard reference material



- A strong need for standard reference material
- Traceable standard dielectric parameters certified by NIST
- Calibration of 5G & 6G material characterization fixtures

- Targeting 0.2% uncertainty in SRM in round robin results
- 9 labs in round robin testing





Round Robin 1



- SRM candidate material fused silica
- 137um thick sample
- Four characterization methods: SCR, SPDR, FPOR, BCDR

35 mm

AM1

45 mm

- Three sample sizes:
 - 90 x 90 mm
 - 35 x 45 mm
 - Dia 30 mm & dia 49 mm
- 9 labs involved

- 90 mm AL1 AS1
- Testing in-plane (FPOR) and out-of-plane (BCDR) anisotropy

SRM candidate









Connecting Minds. Exchanging Ideas.

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Round Robin 1 results (1)



Total of 2991 measurement points



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Round Robin 1 results (2)



Total of 2991 measurement points









Round Robin 1 results (3)



In-plane measurements

Agreement within 1.6%



109



Round Robin 1 results (4)



In-plane measurements



110





Challenges



- 137 um SRM candidates tested
- Handling issues samples breakage:
 - Samples clamping
 - Thickness measurements
 - In between measurements
- Thickness uncertainty \rightarrow potential source of Dk spread
- Round Robin 2:
 - Improving handling 150um thick sample
 - Expected 'strength' to increase ~th^3
 - Thickness measurements insight











Thank you for the attention









5G/6G mmWave Materials and Electrical Test Technology Roadmap

Urmi Ray (iNEMI)



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6G and sub-THz Materials and Test Requirement Roadmap

5G/6G mmWave Materials and Electrical Test Technology (5G/6G MAESTRO) – NIST Advanced Mfg Roadmap

Urmi Ray, iNEMI urmi.ray@inemi.org Presented to: IMS 2023 Industry Workshop www.inemi.org



Advancing manufacturing technology

- Intro to iNEMI
- 5G/6G Communications Overview
- NIST Maestro Roadmap
 - Key Findings
 - Roadmap Table
- Summary and Conclusions



Intro to iNEMI



Advancing manufacturing technology

iNEMI – Premier Industry-led Global Consortium for Electronics Manufacturing Forecast and accelerate improvements in the electronics manufacturing industry





iNEMI Member Value Pyramid







International Electronic Manufacturing Initiative

Industry-led Global Consortium for Electronics Manufacturing



Next Generation mmWave Communication



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5G: Disruptions Enabling next level of communication

- 5G is expected to enable **\$12.3 trillion of global economic output** (almost as much as total US consumer spending in 2016).
- The global 5G value chain will generate **\$3.5 trillion in output** and support **22M jobs** in 2035.
- The 5G value chain will invest an average of **\$200 billion annually** in infrastructure (about half of the total US gov't spending on transportation infrastructure in 2014).



Most 5G-Ready Countries





120

Samsung's 6G Vision

Bring the next hyper-connected experience to every corner of life.



- THz Technology
- Novel Antenna Materials
- Metamaterial antenna to avoid need for phase shifters
- Comprehensive AI
- Split Computing
- Setting up of specifications in 2028, possible rollout in 2028



5G/THZ mmWave- Critical Industry Initiatives Needed

5G Semiconductor Challenges Summary

Challenge	Attractive Approaches	
Need for Antenna in Package (AiP)	 Laminate-based solutions eWLB (FO-WLP) solutions 	
High speed/ Ultra Low Loss materials	 Cost-effective materials at mmWave frequencies Materials characterization and test methods 	
Heterogeneous Integration (SiP)	 Increases in # of components → Miniaturization Advanced molding technologies Shielding 	
Test	 Contact vs OTA testing → still TBD More sensitivities to process variations 	

5G solutions require complex packaging approaches and requires close collaboration.

Source: Heterogeneous Integration Roadmap: 2019



NIST MAESTRO Roadmap Activities

Project Leader: Dr. Urmi Ray, iNEMI Supported by: National Institute of Standards and Technology (NIST) Office of Advanced Manufacturing FEDERAL AWARD ID NUMBER: 70NANB22H050





(a): Potential 6G Spectrum



iNEMI 5G/6G MAESTRO: Partners

Roadmap contributors are leaders in this field from industry, universities and research institutes



For further information and to get involved, please contact Dr. Urmi Ray (urmi.ray@inemi.org)



iNEMI: 5G/6G MAESTRO – Project Objective

Create a technology roadmap

 Develop a comprehensive 10-year hardware roadmap for mmWave materials development & electrical characterization and testing.

Develop a U.S.-focused implementation strategy

- Recommend a U.S.-centric, cross-supply-chain consortium to execute the vision of the roadmap, the foundations for a strong U.S. manufacturing ecosystem in RF materials and testing.
- Promote the growth of a strong and diverse U.S. workforce in RF communication technologies, by proposing a plan of university curricula development and training.



5G/6G MAESTRO: Technology Scope

110GHz-170GHz (D-Band), 220-350GHz (G Band)





iNEMI 5G/6G MAESTRO: Project Flow





NIST MAESTRO Key Findings



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Market Survey Key Findings

Material Needs: Mobile

- RF front end
 - Si CMOS or BEOL CNT-FET CMOS device projected to be used through 2032
 - Laminate substrates, embedded trace substrate (ETS), molded interconnect substrate (MIS), buildup film and wafer level packages (WLP); increased use of WLP projected for 2030-2032
 - PCB layer counts increasing from 8-12 layers, up to 16 layers 2023-24, 16-20 layers 2026-2028, >20 layers in 2030-2032 (however, layer count adds thickness and cost)
- Power amplifier
 - CMOS and GaAs (2023), plus GaN (2024-2032)
 - Laminate substrates and WLP
 - PCB with 4-8 layers (2022-2028), >8 layers (2030-2032)
- Increased use of antenna-in-package (AIP) modules

Based on Survey and 1:1 interviews with industry



Infrastructure

- 5G mmWave capable handsets introduced ahead of infrastructure
- US 5G mmWave infrastructure expected to roll out in 2024 (2X current levels) with peak in 2027 (for U.S., Japan, South Korea, Australia, and Thailand mainly)
 - Slower because of CAPEX and OPEX
- C-band delayed 5G mmWave deployment in US
- 6G expected to roll out 10 years from now
- 6G at concept phase with massive technological foundation that is not fully laid out (a work in progress)

Based on Survey and 1:1 interviews with industry



Market Survey Key Findings

Material Needs: Base Stations, Automotive, Defense

- •Deployment of infrastructure for 5G and future wireless technologies requires a diverse range of materials and packaging technologies to meet performance, cost, and reliability requirements
- •GaAs and GaN are the primary materials used in base stations, with the possibility of SiGe for lower power applications
- •Laminate substrates are widely used, but the use of system-in-package (SiP) is anticipated to increase; glass and glass core substrates expected to mature
- •More use of antenna-in-package (AiP) with unique designs and IC functionality integration without an embedded air cavity
- •Automotive radar modules for 79-81 GHz mmWave use FC-CSP laminate substrates and FO-WLP,. FO-WLP also provides smooth conductor, high-speed material sets, controlled impedance, excellent RF isolation, and reliable performance in harsh conditions. The industry is anticipating mmWave modules for 94-100 GHz in the future.

•The defense industry uses Si, SiGe, GaAs, and GaN devices, with Si CMOS being an important choice for 6G. InP HBT transferred to AIN is also being considered for better thermal capability. The integration of InP and Si is a challenge for package materials, and advanced thermal management techniques are needed. Diamond substrates and micro-fluidic cooling channels are being investigated, and sintered silver die attach processes have been developed. LTCC provides higher reliability due to CTE matching and better thermal dissipation. PCBs will require higher frequency materials, embedded antennas and filters, and improved thermal capability. Defense final test needs require extremely low defect rates with much higher test 1340verage than commercial needs.

Market Survey Key Findings

Electrical Testing and Metrology Needs

- •Testing at multiple levels is required to detect and prevent the small deviations that can cause performance issues
- •Major challenges to overcome overall cost

•While over-the-air (OTA) testing is considered best for development, it is often undesirable in high-volume manufacturing due to cost concerns. The use of wafer-level test and self-test, along with increased use of AI, is expected in the future. Some companies plan to test antennas separately at higher frequencies. Designs are typically centered on 28 GHz, and BIST features are used for electrical testing with some limited OTA.

•To address data concerns, non-destructive metrology tools are needed, such as 3D x-ray coupled with AI/ML to analyze components (e.g., small cells, base stations, and mobile devices) each of which has its own replacement cycles

Based on Survey and 1:1 interviews with industry



Reconfigurable Intelligent Surfaces – Assisted Joint Beamforming

Small wavelengths at 5G/6G mm-Wave frequencies are subject to path losses and multipath scattering leading to beam blockage

Reconfigurable
Intelligent Surfaces (RIS)
supersede relay
performance using large
apertures with simple
circuitry.
✓ Spectrally more efficient
✓ RIS reduce hardware

complexity.



Alternative Technology: Relays

- A dedicated power source per relay
- Reception and retransmission circuitry
- Signal processing complexities.

Use of Meta Materials

Goal: Beamforming and adaptive nulling using RIS via a very simple circuitry



Advanced Packaging Needs: 5G/mmWave Package Integration



E WALLS and H WALLS (PEC and PMC)

- High-density and low-loss transmission lines:
- Ultra-fine vias and TPVs for seamless 3D interconnects
- Precision circuitry for impedance
 matching
- Smooth surface for low losses

- RF and digital in the same package
- Advanced antenna array for wideband and gain
- Embedded FSS for improved performance
 - Heterogeneous high-K superstrates as lenses, E walls, H walls, AMCs
 - Package- and board-level reliability
 - Large-area panel processing



High-Density Packaging: Chip-first mm-wave packages using Fan out Technologies

InFO-AiP (TSMC)



- \checkmark Thickness reduction
- \checkmark Low signal loss from Chip to Antenna
- × Unbalanced stack-up Warpage
- imes Patterning precision on molding compound



- \checkmark Thickness reduction
- \times $\,$ Distance variation from Gnd to Patch
- imes Patterning precision on molding compound

Fan-out AiP (ASE)



Fig. 12. Simulation result of stacking patch antenna

- \checkmark Thickness reduction
- $\times~$ High signal loss in through-mold vias
- \times $\,$ Mold-on-mold causes thickness variations



5G Challenges – Material, Substrate

- Novel Material –better electrical, microwave and thermal properties at a low cost
- Low loss materials, characterization/metrology

Material properties of glass provide attractive solutions







Metrology needs: Semiconductor manufacturer's measurement problem

Semiconductor Industry Association



iNEMI 5G Materials Characterization Project Report I: "The lack of traceable reference material for mmWaves is a very serious problem. This lack makes verification of measurement methods and laboratory techniques impossible in an industry setting."

SRC Research Needs: Packaging

"Dielectric characterization up to 500 GHz and beyond. Scope includes anisotropic and inhomogeneous materials... High-frequency and high-temperature dielectric characterization of lowloss materials (encapsulants, mold compounds, substrates, etc.)."

Dielectric properties impact all microelectronics

manufacturing

Metrology Needs: Accurate, Repeatable Measurements





MAESTRO Roadmap Example: Gaps

Table 5G-3

Materials Characterization Needs, Gaps, and Today's Technology Status with Respect to Current and Future Needs

					In-table color + label key	Description of Technology Status
					Solutions not known	Solutions not known at this time
	TODAY (2023)	3 YEARS (2026)	5 YEARS (2028)	10 YEARS (2033)	Solutions need optimization	Current solutions need optimizatio
					Solutions deployed or known	Solutions deployed or known toda
TECHNOLOGY					Not determined	TBD
ISSUE	Frequency Range= 28-110 GHz	Frequency Range= 110-170 GHz (D-Band)	Frequency Range= 220-350 GHz (G Band)	Frequency Range= >500 GHz		
CHARACTERIZ	ATION FREQUENCY RANGE				-	
NEED	Tools needed at 5G frequencies (28-39 GHz)	Tools needed at D-band (110-170 GHz)	Tools needed G-band (220-350 GHz)	Tools needed for >500 GHz	-	
CURRENT TECHNOLOGY STATUS	Solutions deployed or known	Solutions need optimization	Solutions not known			
GAP	(NO GAP?)	Few tool options	Robustness and availability			
CHALLENGE	Limited tool availability for high frequencies	Supporting equipment is expensive (i.e., 100 GHz VNA)	Expensive supporting e	quipment		
CHALLENGE	High frequencies place burden on mechanical precision of equipment	Methods still in academic space				
CH3ALLENGE	High equipment cost					INEN

MAESTRO Roadmap Example: Potential Solutions

Table 5G-4 Materials Characterization Potential Solutions						
		EXPECTED TRL LEVEL				
TECHNOLOGY ISSUE	POTENTIAL SOLUTIONS	TODAY (2023)	3 YEARS (2026)	5 YEARS (2028)	10 YEARS (2033)	
ANISOTROPIC MATERIAL	Develop new and disruptive methods for material characterization	3	4	5	9	
CHARACTERIZATION	Converge on common sample geometry	3	5	7	9	
	"Cherry pick" samples	9	9	9	9	
SAMPLE THICKNESS VARIATION	Use of mechanical methods to modify existing samples to improve thickness uniformity	4	4	4	4	
	Develop new methods with less sensitivity to thickness variation	1	2	3	5	

Color and Range of Technology Readiness Levels (TRL)	Description		
TRL: 1 to 4	Levels involving research		
TRL: 5 to 7	Levels involving development		
TRL: 8 to 9	Levels involving deployment		



https://www.inemi.org/maestro

Summary and Conclusions



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5G/THZ mmWave- Need for Collaborative Approaches

5G Semiconductor And Package Integration Challenges Summary

- New Materials, Packaging and Testing Needs
- Complexity of challenges including architecture to overcome losses (including hybrid beamforming)
- Collaborative pre-competitive approaches are needed
- Many new areas of research are emerging opportunities for ground breaking research and development



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